DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
INSTITUTE OF TECHNOLOGY
UNIVERSITY OF KASHMIR
NAAC Accredited Grade – "A+"

Revised Date Sheet of End Term Examination for M.Tech ECE, IOT, 1st & 3rd semesters is as follows:

Time: 11:00AM

Date	IST/3rd Semester
Mon, Jul 8, 2024	Analog Integrated Circuit Design
Wed, Jul 10, 2024	Research Methodology and Language/Computer Vision
Fri, Jul 12, 2024	DIP/Coding Techniques
Mon, Jul 15, 2024	Advanced DSP/Advanced Digital Communication

Sd/-

Examination Incharge
Deptt of ECE

Copy to:

1. Dean, School of Engineering, University of Kashmir.

2. Director, Institute of Technology

3. Controller Examination, University of Kashmir.

4. Heads/ Coordinators (all branches), Institute of Technology

5. Concerned Faculty Members

6. Notice Board