Mir Yavar Hayat yavarhayatm@gmail.com, Srinagar, Jammu and Kashmir

EDUCATION

MITx

Micromasters in Data Science and Machine Learning

SMVD UNIVERSITY

M.Tech in Electronic and Communication

UNIVERSITY OF KASHMIR

B.Tech. in Electronic and Communication

PROJECTS

- Low Voltage Design of Activation Functions and Neural Networks Using Sinh Companding.
- Investigation of Thermoelectric Generator for Increasing Efficiency of Photovoltaic Thermal Systems.

SOFTWARES, LANGUAGES AND TOOLS

 Python, MATLAB, Arduino, SPICE, Multisim, Eagle, Mentor Graphics, COMSOL Multiphysics, C programming

RELEVANT EXPERIENCE

Teaching Assistant, SMVD University

Aug 2018 - Jan 2019

Trained undergraduate students on circuit simulation tool (Mentor Graphics).

QUALIFIYING EXAMINATIONS

- · UGC NET in Electronics Science
- GATE in Electronics and Communication Engineering
- GATE in Data Science and Artificial Intelligence

CONFERENCE PAPERS

- Mir Yavar Hayat, Purnima Hazra, "Recent Developments on Design Parameter Optimization to Enhance the Performance of Photovoltaic-Thermoelectric Generator Systems", International Conference on Recent Advances in Interdisciplinary Sciences, University of Jammu.
- Mir Yavar Hayat, Purnima Hazra, "Mathematical Modelling and Performance Analysis of Spectrum Splitting Photovoltaic Thermoelectric Generator System", 3rd International Conference on Condensed Matter & Applied Physics (ICC-2019), Bikaner.

TRAINING PROGRAMS & WORKSHOPS

- 4 weeks industrial training in BRBRAIT, Jabalpur, Madhya Pradesh.
- 2 weeks refresher course on *Introduction to MATLAB and Research Enhancement Tools* organised by School of Electronics & Communication Engineering, SMVDU, Katra.