



INSTITUTE OF TECHNOLOGY

University of Kashmir

Zakura Campus, Srinagar 190006

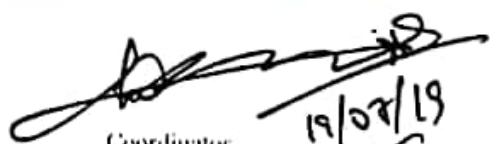
No. 1 (Date Sheet, 4th Semester/ECE) 1017/KI/2019

Dated: 19/07/2019

Date Sheet

End Semester Examination for Regular & Backlog Candidates of B.Tech, ECE-4th Semester, Zakura Campus,
Centre: Block B, 1st Floor, Lecture Halls B109, B110 (CBF1 & CBF2)

Date/ Day	Time	Course Name(Course Code)	
		Regular	Backlog
01/08/2019 (Thursday)	11:00 A.M.	Electromagnetic Fields and Waves ECE4417B	Electromagnetic Fields and Waves (ECE4517) Electromagnetic Fields and Waves (ECE4417B)
03/08/2019 (Saturday)	11:00 A.M.	Communication Systems- I ECE4317B	Communication Systems- I (ECE4417) Communication Systems- I (ECE4317B)
06/08/2019 (Tuesday)	11:00 A.M.	Analog Electronic Circuits- II ECE4217B	Control Systems (ECE4317) Analog Electronic Circuits-II (ECE4217B)
08/08/2019 (Thursday)	11:00 A.M.	Digital Electronics and Logic Design ECE4517B	Microprocessors(ECE4617) Digital Electronics and Logic Design(ECE4517B)
10/08/2019 (Saturday)	11:00 A.M.	Engineering Mathematics – IV MTH4117B	Engineering Mathematics – IV(MTH4117) Engineering Mathematics – IV(MTH4117B)


Coordinator
(ECE)
19/07/19

Copy To:

1. Dean, Faculty of Engineering, University of Kashmir,
2. Controller of Examinations, University of Kashmir,
3. Director, Institute of Technology, University of Kashmir
4. Notice Board,
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