

INSTITUTE OF TECHNOLOGY
Zakura Campus, University of Kashmir
w.e.f (22-05-2018)

TIME TABLE FOR 6 th SEM ECE						
Day/Time	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-3:00	3:00-4:00
Monday	Communication system Lab-II (ECE-LS3)/ Power electronics lab-I (Power systems lab)		Engineering Mathematics-v	Nimaz&Lunch Break	Computer organization and architecture	Data structures
Tuesday	Communication system Lab-II (ECE-LS3)/ Power electronics lab-I (Power systems lab)		Computer organization and architecture		Communication-II	Engineering Mathematics-v
Wednesday	Data structure lab(COMP LAB) /Vlsi design lab(LS4)		Engineering Mathematics-v		VLSI design	Data structures
Thursday	Data structure lab(COMP LAB) /Vlsi design lab(LS4)		Power electronics		Communication-II	VLSI design
Friday	Computer organization and architecture	VLSI design	Power electronics		library	Communication-II
Saturday	Engineering Mathematics-v	Data structures	Power electronics		Communication-II	EXTRA CARRICULAR ACTIVITIES

Venue for theory classes: block A

Course In-charge:

Communication-II:

Theory: Er. A. Mueed Hafiz

Lab: Er. A. Mueed Hafiz

Power electronics:

Theory: Er.Ubaid

Lab: Er.Ubaid/Er.Naira

Computer organization and architecture. Er. Umhara

Engineering Mathematics-v: Dr. Abid

Data structures:

Theory: Concerned faculty/ Dr. Bilal

Lab: Concerned faculty/ Dr. Bilal

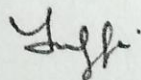
VLSI design

Theory: Er. Abeena

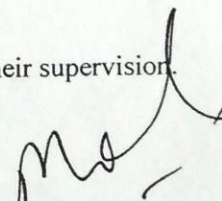
Lab: Er. Abeena / Dr. Bilal

Note:

- The students will be bifurcated into two equal groups.
- Teachers should manage equal lab-hours for both the student groups under their supervision.



Coordinator(ECE)



Director