



Office of the Director
Institute of Technology, University of Kashmir,
Zakura Campus, Srinagar

No: F(datesheet/Major/Odd_sem/Spring_2021/DoECE)IoT/KU/21
 Date: 04/06/2021

Online Major (Theory and lab) Examination schedule for B.E/B.Tech 3rd, 5th and 7th semesters (Regular/ backlog) of Electronics & Communication Engineering

| Date | MCQs and short answer type (35 Marks) (09:30 AM to 10:10 AM) | Viva Voce (15 Marks) (10:30 AM onwards) | | |
|---------------------------|---|---|---|--|
| | (IoT+SSM) (Regular/ Backlog) | 3rd Semester (Regular/ Backlog) | 5th Semester (Regular/ Backlog) | 7th Semester (Regular/ Backlog) |
| 09/06/2021 (Wednesday) | VLSI Design [ECE-7117B/7117] | | | VLSI Design [ECE-7117B/7117] (Theory and Lab) (IoT) |
| | | | | Data Communication [theory and lab] [ECE-7217B] (SSM- 01 to 70) |
| | | | | Microwave Engineering/ Microwave and Radar Engineering (Theory and Lab) [ECE-7317B/ ECE-7417] (SSM- 71 & onwards and backlog) |
| 10/06/2021 (Thursday) | Signals and Systems [ECE-3417B/ 3417] | Signals and Systems [ECE-3417B/ 3417] (IoT) | | |
| | | Material Science [ECE-3517B] (SSM 01-60) | | |
| | | Analog Electronics-I (theory and Lab) [ECE-3317B/3317] (SSM-61 & Onwards ad Backlog) | | |
| 11/06/2021 (Friday) | Data Communication[ECE-7217B/ECE-7217] | | | VLSI Design [ECE-7117B] (Theory and Lab) (SSM- 01 to 70) |
| | | | | Data Communication [theory and lab] [ECE-7217B/7217] (SSM- 71 & onwards and backlog) |
| | | | | Microwave Engineering/ Microwave and Radar Engineering (theory and lab) [ECE-7317B/ ECE-7417] (IoT) |
| 14/06/2021 (Monday) | Material Science [ECE-3517B/ECE 3617] | Signals and Systems [ECE-3417B/ 3417] (SSM 01-60) | | |
| | | Material Science [ECE-3517B/3617] (SSM-61 & Onwards ad Backlog) | | |
| | | Analog Electronics-I (theory and lab) [ECE-3317B/3317] (IoT) | | |
| 15/06/2021 (Tuesday) | Microwave Engineering/ Microwave and Radar Engineering [ECE-7317B/ ECE-7417] | | | VLSI Design [ECE-7117B/7117] (Theory and Lab) (SSM- 71 & onwards and backlog) |
| | | | | Data Communication [theory and lab] [ECE-7217B/7217] (IoT) |
| | | | | Microwave Engineering (theory and lab) [ECE-7317B] (SSM- 01 to 70) |
| 16/06/2021 (Wednesday) | Analog Electronics-I [ECE-3317B/3317] | Signals and Systems [ECE-3417B/ 3417] (SSM-61 & Onwards ad Backlog) | | |
| | | Material Science [ECE-3517B/3617] (IoT) | | |
| | | Analog Electronics-I (theory and lab) [ECE-3317B/3317] (SSM 01-60) | | |
| 17/06/2021 (Thursday) | Elective-I (AI & ML)[ECE-7**17BE/[ECE-7**17E] Elective-I (OFC/OC)[ECE-7**17BE] & OFC[7117] | | | Power system (theory and lab) [ECE-7417B/ECE-7517] (IOT) |
| | | | | Elective-I (OFC/OC & AIML)[ECE-7**17BE] & OFC [ECE 7117] (SSM 01 - 70) |
| 18/06/2021 (Friday) | Electrical Machines [ECE-5217B/5617] | | Electrical Machines (theory and Lab) [ECE-5217B/5617] (IoT) | |
| | | | Control System (Theory and Lab) [ECE-5517B] (SSM- 01 to 70) | |
| | | | Digital Signal Processing (ECE-5117B/5117) (SSM- 71 onwards and Backlog) | |
| 21/06/2021 (Monday) | Industrial Organisation and Management [ECE-7317B] | | | Power system (theory and lab) [ECE-7417B/ECE-7517] (SSM 01- 70) |
| | | | | Elective-I (OFC/OC & AIML)[ECE-7**17BE/ECE-74817E] (SSM- 71 onwards & backlogs) |
| | | | | Industrial Organisation and Management (IOT + SSM) |

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|---------------------------|---|--|---|--|
| 22/06/2021 (Tuesday) | Network Analysis And Synthesis [ECE-3217B/ECE3217] | Network Analysis And Synthesis (theory and lab) [ECE-3217B/ECE3217] (IoT) | | |
| | | Engineering mathematics-III [MTH-3117B] (SSM 01- 60) | | |
| | | EDA TOOLS LAB [ECE-3617BL/ECE3717L] (SSM-61 & Onwards and Backlog) | | |
| 23/06/2021 (Wednesday) | Control System [ECE-5517B] | | Electrical Machines (theory and Lab) [ECE-5217B/5617] (SSM- 01 to 70) | |
| | | | Control System (Theory and Lab) [ECE-5517B] (SSM- 71 onwards and backlog) | |
| | | | Digital Signal Processing (theory and Lab) (ECE-5117B/5117) (IoT) | |
| 24/06/2021 (Thursday) | Power system [ECE-7417B/ECE-7517] | | | Power system (theory and lab) [ECE-7417B/ECE-7517] (SSM- 71 & onwards and backlog) |
| | | | | Elective-I [ECE-7**17BE] (IoT) |
| 25/06/2021 (Friday) | | Network Analysis And Synthesis (theory and lab) [ECE-3217B/ECE3217] (SSM-61 & onwards and Backlog) | | |
| | | Engineering Mathematics-III [MTH-3117B/MTH 3117] (IoT) | | |
| | | EDA TOOLS LAB [ECE-3617BL] (SSM- 01 to 60) | | |
| 28/06/2021 (Monday) | | | Electrical Machines (Theory and Lab) [ECE-5217B/5617] (SSM- 71 onwards and backlog) | |
| | | | Control System (Theory and Lab) [ECE-5517B] (IoT) | |
| | Digital Signal Processing (ECE-5117B/5117) | | Digital Signal Processing(Theory and lab) (ECE-5117B) (SSM- 01 to 70) | |
| 29/06/2021 (Tuesday) | | Network Analysis And Synthesis (theory and lab) [ECE-3217B/ECE3217] (SSM 01-60) | | |
| | Engineering Mathematics-III (MTH-3117B/3117) | Engineering mathematics-III [MTH-3117B] (SSM-61 &onwards and backlog) | | |
| | | EDA TOOLS LAB [ECE-3617BL/ECE3717L] (IoT) | | |
| 30/06/2021 (Wednesday) | Microprocessors/Microcontrollers and Embedded Systems[ECE-5417B/ECE-5317] | | Microprocessors/Microcontrollers and Embedded Systems (theory and Lab) [ECE-5417B/ECE-5317] (IoT) | |
| | | | DCIT [ECE-5317B] (SSM- 01 to 70) | |
| 02/07/2021 (Thursday) | | | Microprocessors/Microcontrollers and Embedded Systems (theory and Lab) [ECE-5417B/ECE-5317] (SSM- 71 onwards and backlog) | |
| | DCIT [ECE-5317B/ECE-5417] | | DCIT [ECE-5317B/ECE-5417] (IoT) | |
| 03/07/2021 (Friday) | Elective-I (Mobile Computing) [ECE-7217BE] | | Microprocessors (theory and Lab) [ECE-5417B] (SSM- 01 to 70) | Elective-I (mobile Computing) [ECE-7217BE] (IoT & SSM) |
| | | | DCIT [ECE-5317B] (SSM- 71 onwards & backlog) | |
| | Solid State Devices [ECE-5517] | | Solid State Devices [ECE-5517] (IoT & SSM) | |
| 06/07/2021 (Monday) | Electronic Instrumentation and PLC [ECE-5217] | | Electronic Instrumentation and PLC (theory and Lab) [ECE-5217] (IoT & SSM) | |
| 07/07/2021 (Tuesday) | Digital Electronics and Logic design [ECE-3517] | Digital Electronics and Logic design (theory and Lab) [ECE-3517] (IoT & SSM) | | |

Instructions for Students

- 1) The examination will be held in dual mode comprising of (MCQ component , short answer type component) and Viva component. The MCQ+short answer type component will be held via Google Forms & Viva components will be held via Google meet / Zoom, platforms. However the examination of the Machine Drawing will be held via Google Forms (MCQs) and Google Classroom (subjective questions)
- 2) The MCQ component of 25 questions (each of 1 marks) or amalgam of 1 and 2 mark questions, the short answer type component shall carry weightage of 10 marks with two questions (5 marks each) or three questions (4, 3, 3 marks respectively), the viva component will carry a weightage of 15 marks.
- 3) For the purpose of smooth conduct of the evaluation, a WhatsApp group shall be created and maintained by concerned examiner
- 4) The online Zoom link for the evaluation shall be shared by the concerned examiner.
- 5) All students shall name their profiles with the last three digits of their University enrolment numbers followed by underscore followed by their first name, e.g. 005_amir, 072_mehak, 118_zubair. The concerned examiners must ensure the same.
- 6) Students must keep video mode on while appearing for viva
- 7) Students must attend the viva in a room without any background noise; the room must be properly illuminated, failing which student will be disqualified from appearing in examination

8) Any student who won't follow above guidelines will be barred from appearing in viva.

9) In case any student is not able to attend viva due to internet blockade in any district or weak internet connectivity he/she shall appear for offline viva in campus.

10) Students are strictly directed to use only Google chrome for accessing the Google Form.

11) Students are directed not to switch between windows once they are on the Google Forms page & neither use any third party/specialized software with whatsoever intentions. Students shall be responsible for any technical issue arising out of the said activity that prevents his/her response from being submitted.

Sd/-

Examination Incharge

Department of Electronics and Communication Engineering

Sd/-

Coordinator

Department of Electronics and
Communication Engineering

Sd/-

Director

Institute of Technology, University of Kashmir

Copy to:

1. Dean, Academic Affairs, University of Kashmir.
2. Dean, School of Engineering, University of Kashmir.
3. Controller Examination, University of Kashmir.
4. Heads/ Coordinators (all branches), Institute of Technology
5. Principal, SSM college of Engineering and Technology
6. Concerned Faculty Members