



OFFICE OF THE DEAN ENGINEERING

SCHOOL OF ENGINEERING

UNIVERSITY OF KASHMIR (NAAC ACCREDITED A+)

F (Major-Exam-Oct/Nov-ECE) DSE/KU/21

Dated: 1st January 2021

Online Examination Schedule of 1st & 3rd Semester (Backlog) Batches 2017 & 2018 only

B.E./B.Tech. Electronics and communication Engineering

Date	1 st Semester (2017 & 2018 batches)		3 rd Semester (2017 batch)	
	10:30 a.m. to 11:00 a.m.		2:30 p.m. to 3:00 p.m.	
07/01/2021	MTH-1117B	Engineering Mathematics-I	MTH3117B	Engineering Mathematics - III
08/01/2021	PHY-1217B	Engineering Physics	ECE3217B	Network Analysis and Synthesis
09/01/2021	ELE-1317B	Basic Electrical Engineering	ECE3317B	Analog Electronic Circuits -1
10/01/2021	CSE-1417B	Fundamentals of Computer Programming	ECE3417B	Signals & Systems
11/01/2021	HUM-1517B	Communication Skills	ECE3517B	Material Science
12/01/2021	MEE-1617B	Engineering Drawing*		

Lab Examination Schedule

	Lab	Time (IOT)	Time (SSM)	Lab	Time (IOT)	Time (SSM)
13/01/2021	Basic Electrical Engg Lab	10:30 a.m.	11:30 a.m.	Network analysis and synthesis Lab	10:30 a.m.	11:30 a.m.
	Fundamentals of Computer Programming Lab	12:00 p.m.	2:00 p.m.	Analog Electronic Circuits lab	12:00 p.m.	2:00 p.m.
14/01/2021	Engineering Physics Lab	10:30 a.m.	11:30 a.m.	EDA Tools Lab	----	

*** Mode and timing of Drawing Exam will be notified separately**

Note: Only those students will be allowed to appear in the Examination that submit their Examination form and fees before start of examination.

Prof. S. M. A. Andrabi
(Dean School of Engineering)

Copy to

1. Controller Examination, University of Kashmir
2. Principal SSM College of Engineering & Technology
3. Head/Coordinators, Institute of Technology
4. Website